



Espacenet

Bibliographic data: JP 6319261 (A)

MULTIPLE OUTPUT CONVERTER

Publication date: 1994-11-15
Inventor(s): NOJI HIDEO +
Applicant(s): NEC FUKUSHIMA LTD +
Classification: - international: **H02M3/28; H02M3/335;** (IPC1-7): H02M3/28
 - european: H02M3/335M
Application number: JP19920142529 19920603
Priority number(s): JP19920142529 19920603
Also published as:

- JP 2793435 (B2)
- US 5386359 (A)

Abstract of JP 6319261 (A)

PURPOSE: To provide a multi-output converter with three or more output circuits, which is suitable for a reduced loss, the reduced number of components, minialurization, high reliability and the lowering of a price. **CONSTITUTION:** Stabilized output voltage Vo1 of a first output circuit 200, output voltage Vo2 of a second output circuit 300 and output voltage Vo3 of a third output circuit 400 are obtained from voltage Vi of a direct-current voltage supply 6. The output voltage Vo1 is stabilized by a frequency modulation technique of a switching element 110, a resonance circuit 120, an error detecting circuit 600 and a modulation circuit 500. The output voltage Vo2 is stabilized by a pulse modulation technique of the switching element 110, the error detecting circuit 600 and the modulation circuit 500. The output voltage Vo3 is stabilized by controlling the resonance frequency of a variable resonance circuit 410 by a resonance frequency modulation circuit 430 on the basis of the error voltage of the output voltages.

Last updated:
 26.04.2011 Worldwide
 Database 5.7.22; 93p

